

# Abstracts

## A Technique for Correction of Parasitic Capacitance on Microwave f<sub>sub t/</sub> Measurements of MESFET and HEMT Devices (Short Papers)

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*M. Feng, C.L. Lau and C. Ito. "A Technique for Correction of Parasitic Capacitance on Microwave f<sub>sub t/</sub> Measurements of MESFET and HEMT Devices (Short Papers)." 1991 Transactions on Microwave Theory and Techniques 39.11 (Nov. 1991 [T-MTT]): 1880-1882.*

The current gain cutoff frequency, f<sub>sub t/</sub>, has become a critical figure-of-merit for evaluating performance of MESFET and HEMT devices. The f<sub>sub t/</sub> is related to a capacitance parameter, C<sub>sub tot/</sub>, through the equation f<sub>sub t/</sub> = G<sub>m</sub>/(2πC<sub>sub tot/</sub>). This capacitance, however, includes a parasitic component primarily due to contact pad and device geometry as well as a parasitic component due to R<sub>d</sub>, R<sub>s</sub> and R<sub>ds</sub>. This paper describes a technique which determines this parasitic capacitance for FET-type devices. Consistently accurate corrections can then be made to reported f<sub>sub t/</sub> values. Ion implanted InGaAs MESFETs with 0.25 μ gate lengths have achieved 120 GHz f<sub>sub t/</sub>, before correction and 151 GHz f<sub>sub t/</sub> after correction.

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